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Pt s3/5
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DIALOG(R) File 351: DERWENT WPI
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WPI Acc No: 1995-208022/199528
ERPX Acc No: N95-162983
 Transmission of digital signals in ATM communication networks - has ATM
 cells organised in number of fields such that processing time is
 acceptable
Patent Assignee: SIEMENS AG (SIEI )
Inventor: FUNDNEIDER O; LUTZ K A; LOEBIG N; LUTZ K
vumber of Countries: 004 Number of Patents: 005
Patent Family:
Patent No
              Kind
                   Date
                             Applicat No
                                            Kind
                                                   Date
              C1 19950614 DE 4343720
E 4343720
                                             Α
                                                 19931221 199528 B
              A2 19950726 EP 94120227
IP 664630
                                             Α
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CA 2138450
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JP 7202912
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                   19950804 JP 94317260
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                                                           199540
JS 5581551
               Α
                   19961203 US 94360629
                                             Α
                                                 19941221
                                                           199703
                             US 95407380
                                             Α
                                                 19950320
Priority Applications (No Type Date): DE 4343720 A 19931221
'atent Details:
'atent No Kind Lan Pg
                         Main IPC
                                     Filing Notes
DE 4343720
              C1 9 H04J-003/24
3P 664630
              A2 G 10 H04L-012/64
JP 7202912
              Α
                    6 H04L-012/28
'IS 5581551
              Α
                     9 H04J-003/02
                                     Cont of application US 94360629
CA 2138450
              Α
                      H04L-012/54
Abstract (Basic): DE 4343720 C
        An asynchronous transfer mode, ATM, communication system has a
    coupling field (ASN) as the basis of an ATM node. Each node has inputs
    for packeting stages (PAK) that connect with communication lines.
    Outputs are generated by stages that reverse the process (DPAK). The
    ATM cells are arranged in 24 fields each handling two octets that
    allows the time for forming the information packets to be reduced to an
   'acceptable level.
        USE/ADVANTAGE - Reduced data handling time. Used for speech data.
        Dwg.1/5
'itle Terms: TRANSMISSION; DIGITAL; SIGNAL; ATM; COMMUNICATE; NETWORK; ATM;
  CELL; ORGANISE; NUMBER; FIELD; PROCESS; TIME; ACCEPT
Derwent Class: W01; W02
international Patent Class (Main): H04J-003/02; H04J-003/24; H04L-012/28;
  H04L-012/54; H04L-012/64
international Patent Class (Additional): H04L-012/56; H04L-012/66;
  H04Q-003/00; H04Q-011/04
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              $11.28 3 Type(s) in Format 5
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